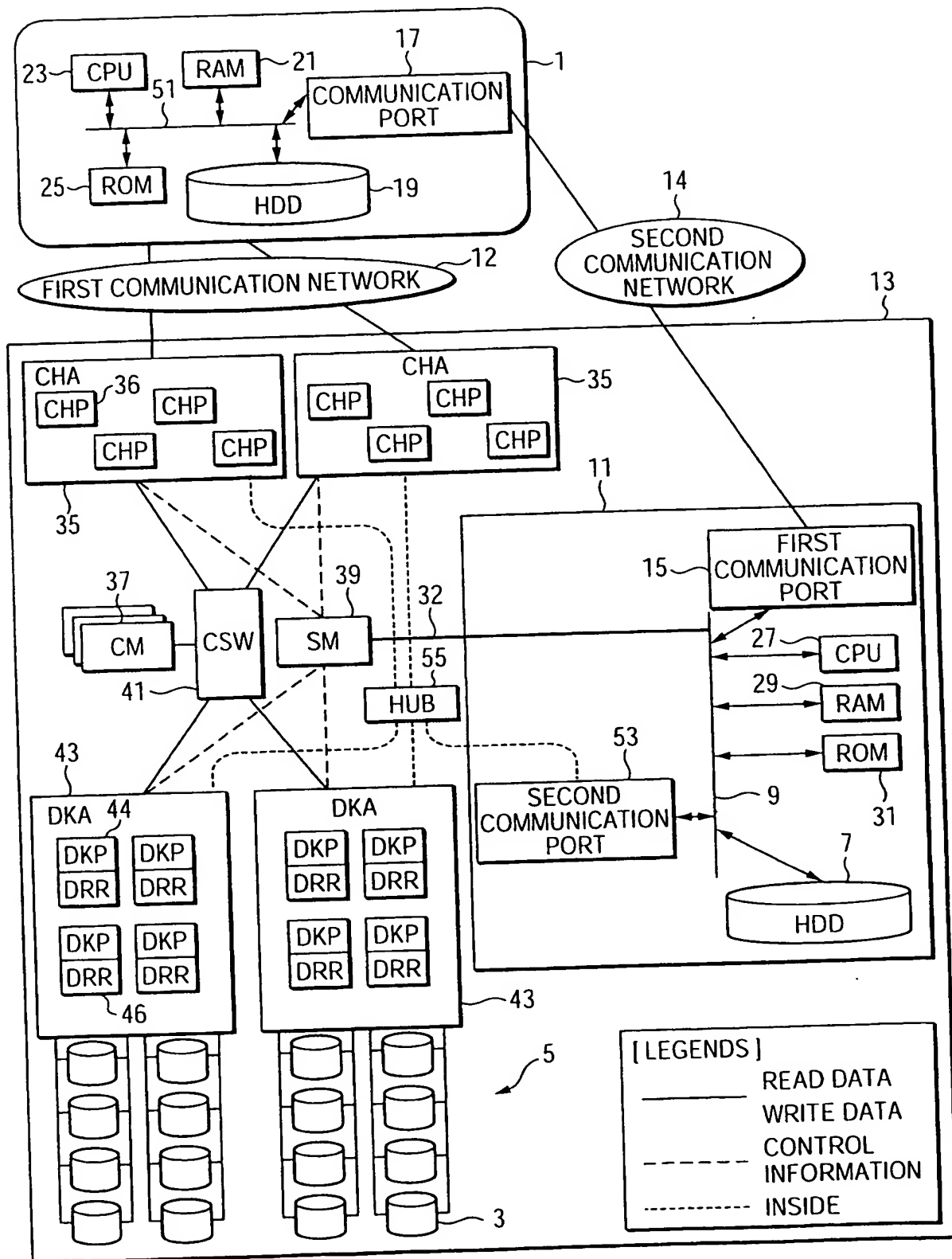


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FIG. 1



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FIG. 2

#	HEADING	ITEM
1	PROCESSOR PERFORMANCE	CHP OPERATING RATE (%)
2		DKP OPERATING RATE (%)
3		DRR OPERATING RATE (%)
4	CACHE PERFORMANCE VALUE	CACHE USE RATE (%)
5		WRITE PENDING (%)
6		SIDE FILE USAGE (%)
7		ASYNC WRITE PENDING (%)
8		READ HIT RATE (%)
9	ACCESS PATH OPERATING RATE	CHA SM AccessPath (%)
10		DKA SM AccessPath (%)
11		CHA CM AccessPath (%)
12		DKA CM AccessPath (%)
13	PORT PERFORMANCE	THROUGHPUT (IOPS)
14		DATA TRANSFER RATE (MB/s)
15		RESPONSE TIME [ms]
16	WWN PERFORMANCE	THROUGHPUT (IOPS)
17		DATA TRANSFER RATE (KB/s)
18		RESPONSE TIME [ms]



CONNECTED TO FIG. 3

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FIG. 3

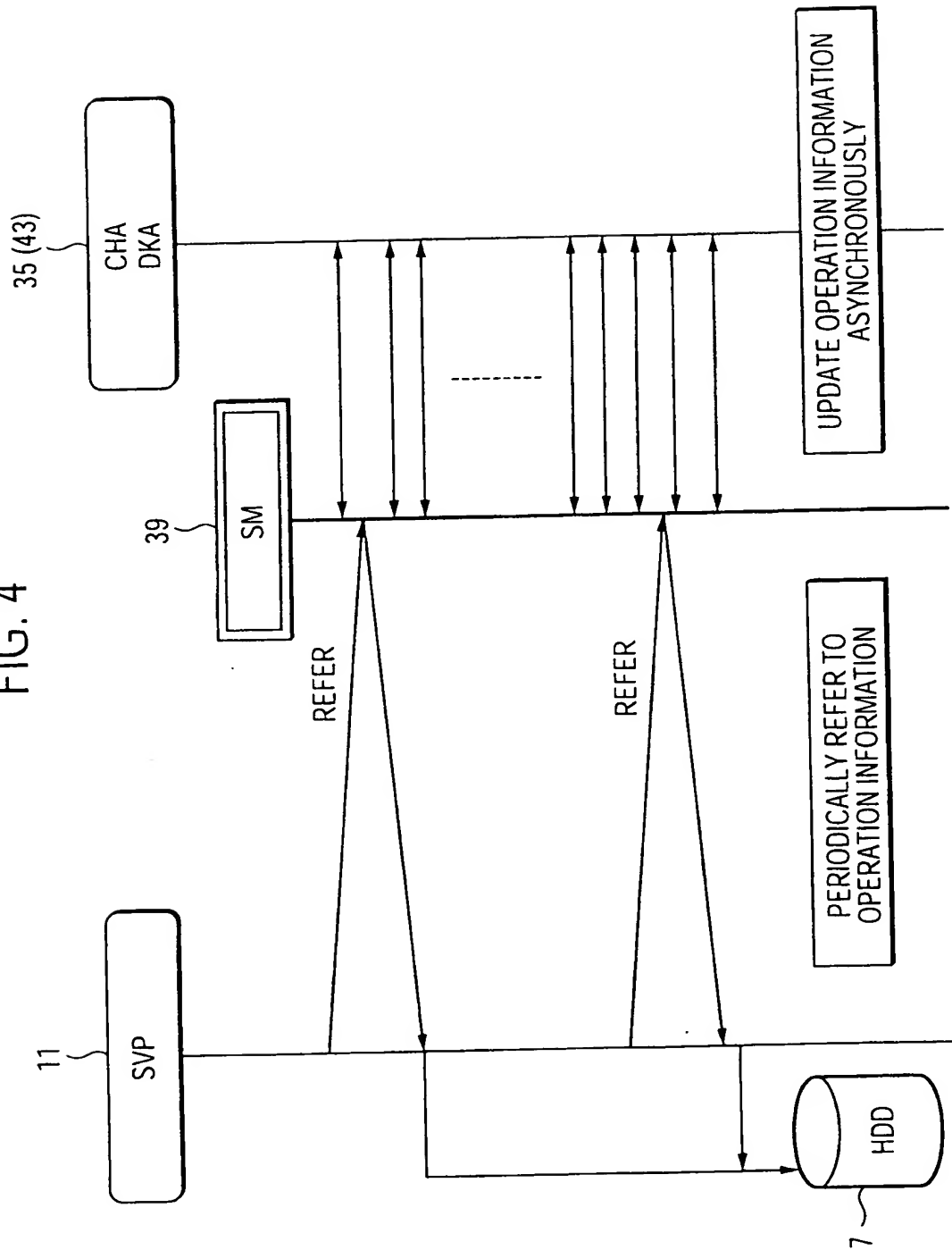
CONNECTED TO FIG. 2



19	LUN PERFORMANCE	THROUGHPUT (IOPS)
20		DATA TRANSFER RATE (MB/s)
21		READ IO PERFORMANCE (IOPS)
22		WRITE IO PERFORMANCE (IOPS)
23		READ HIT RATE (%)
24		WRITE HIT RATE (%)
25		BACKEND PERFORMANCE (COUNTER NUMBER)
26	PG/LDEV PERFORMANCE	THROUGHPUT (IOPS)
27		DATA TRANSFER RATE (MB/s)
28		READ IO PERFORMANCE (IOPS)
29		WRITE IO PERFORMANCE (IOPS)
30		READ HIT RATE (%)
31		WRITE HIT RATE (%)
32		BACKEND PERFORMANCE (COUNTER NUMBER)
33		DRIVE OPERATING RATE (%)
34		R/W SEQ. /RND. ACCESS RATIO

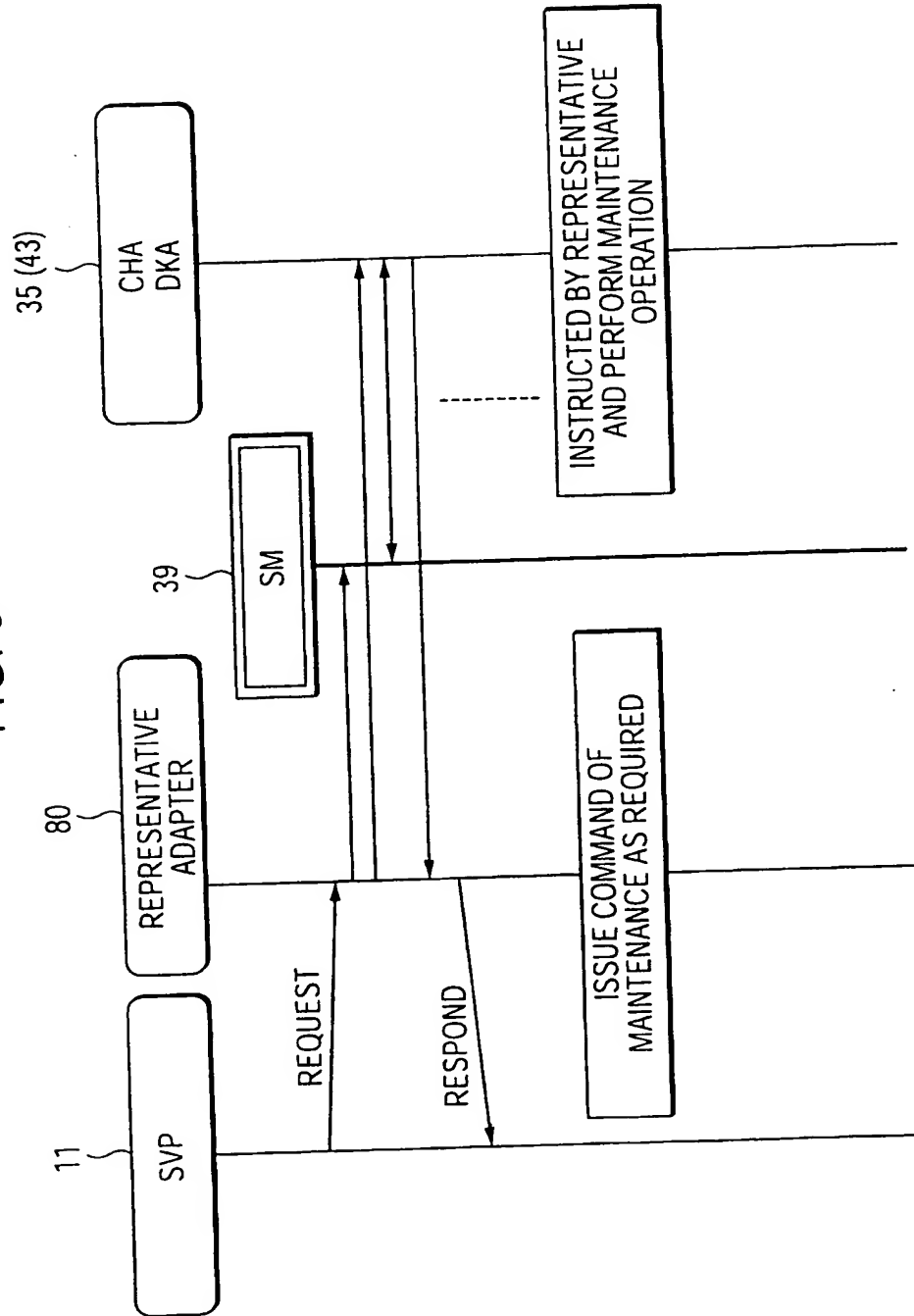
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FIG. 4

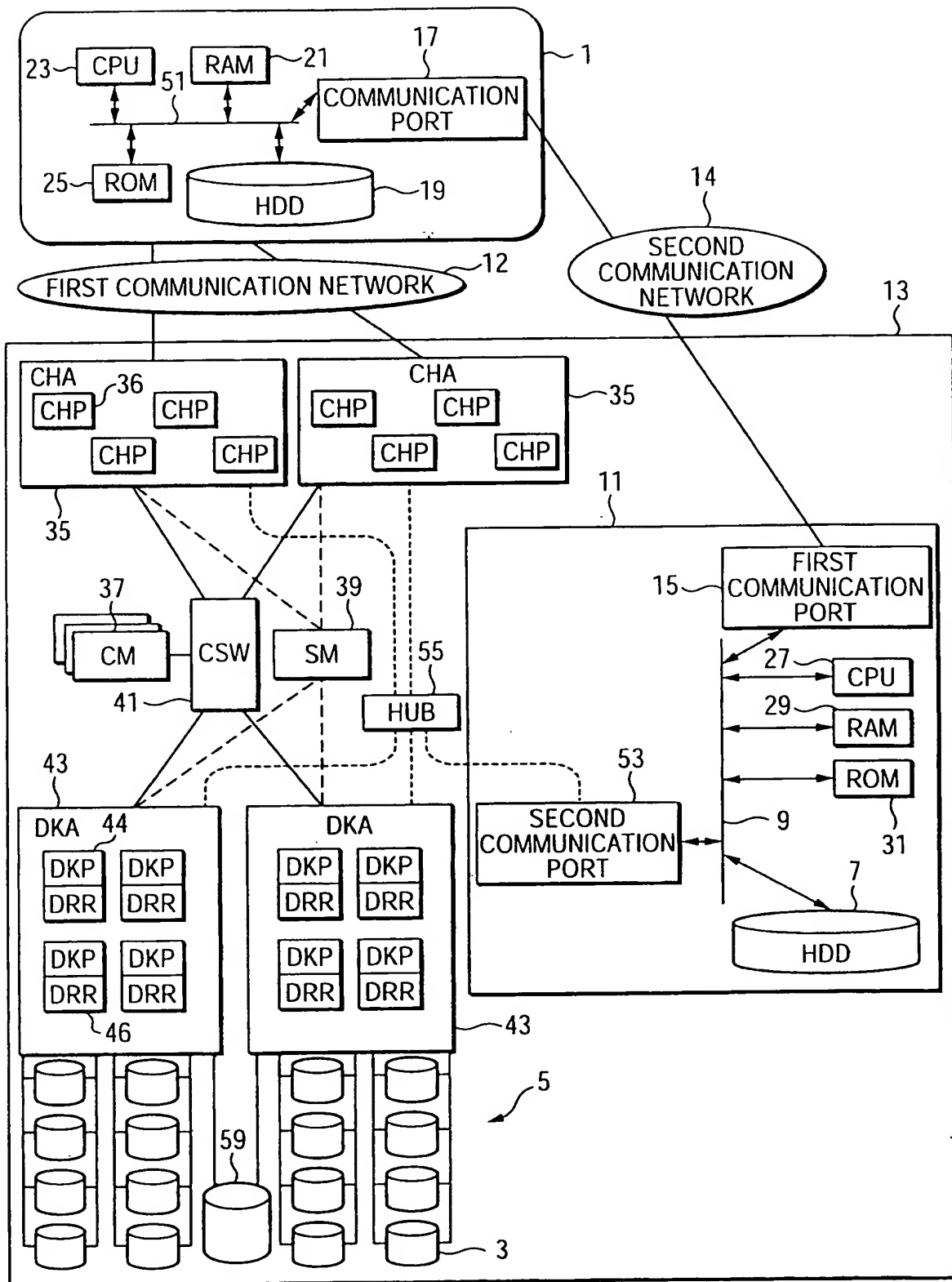


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FIG. 5

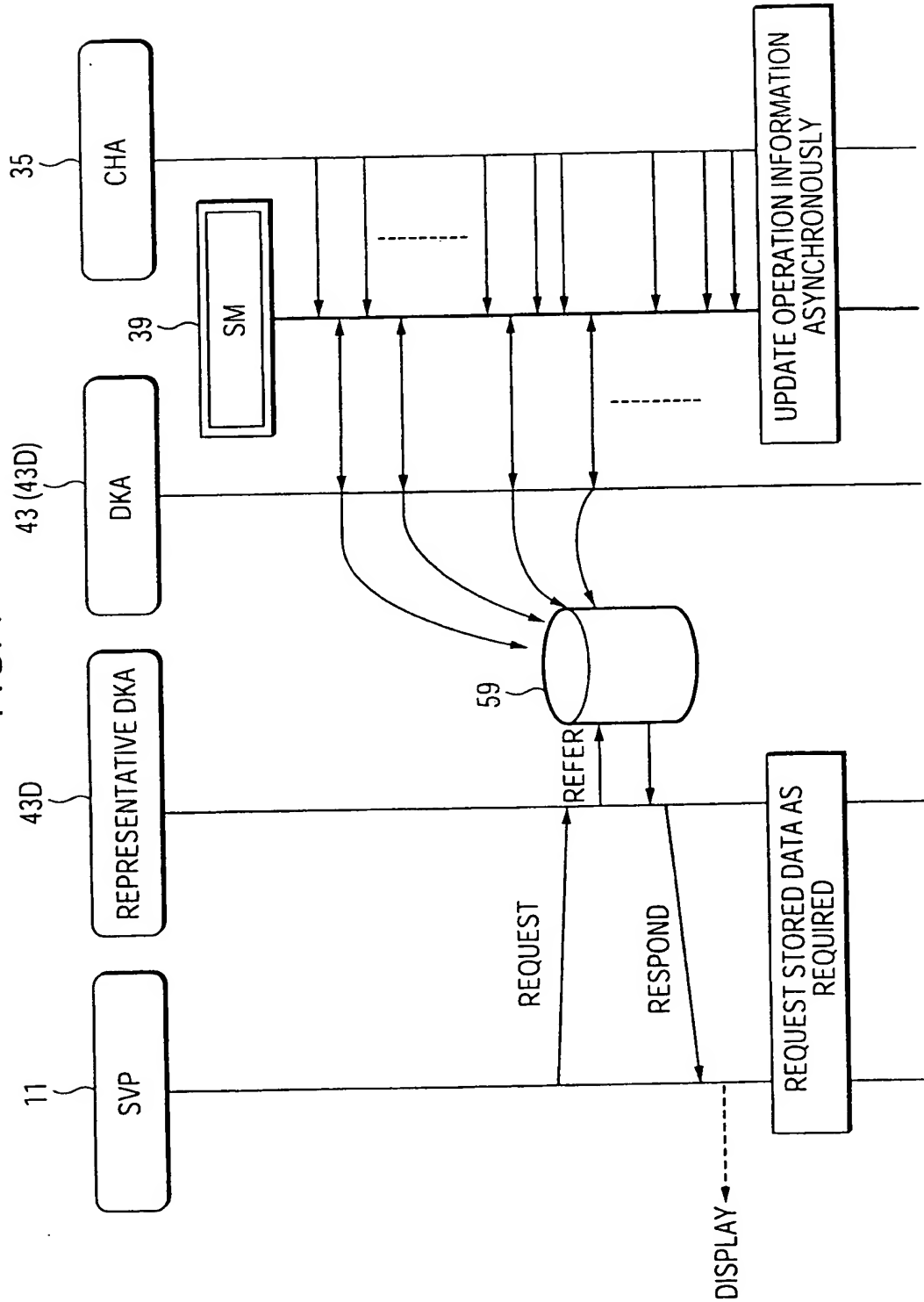


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FIG. 6



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FIG. 7



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FIG. 8

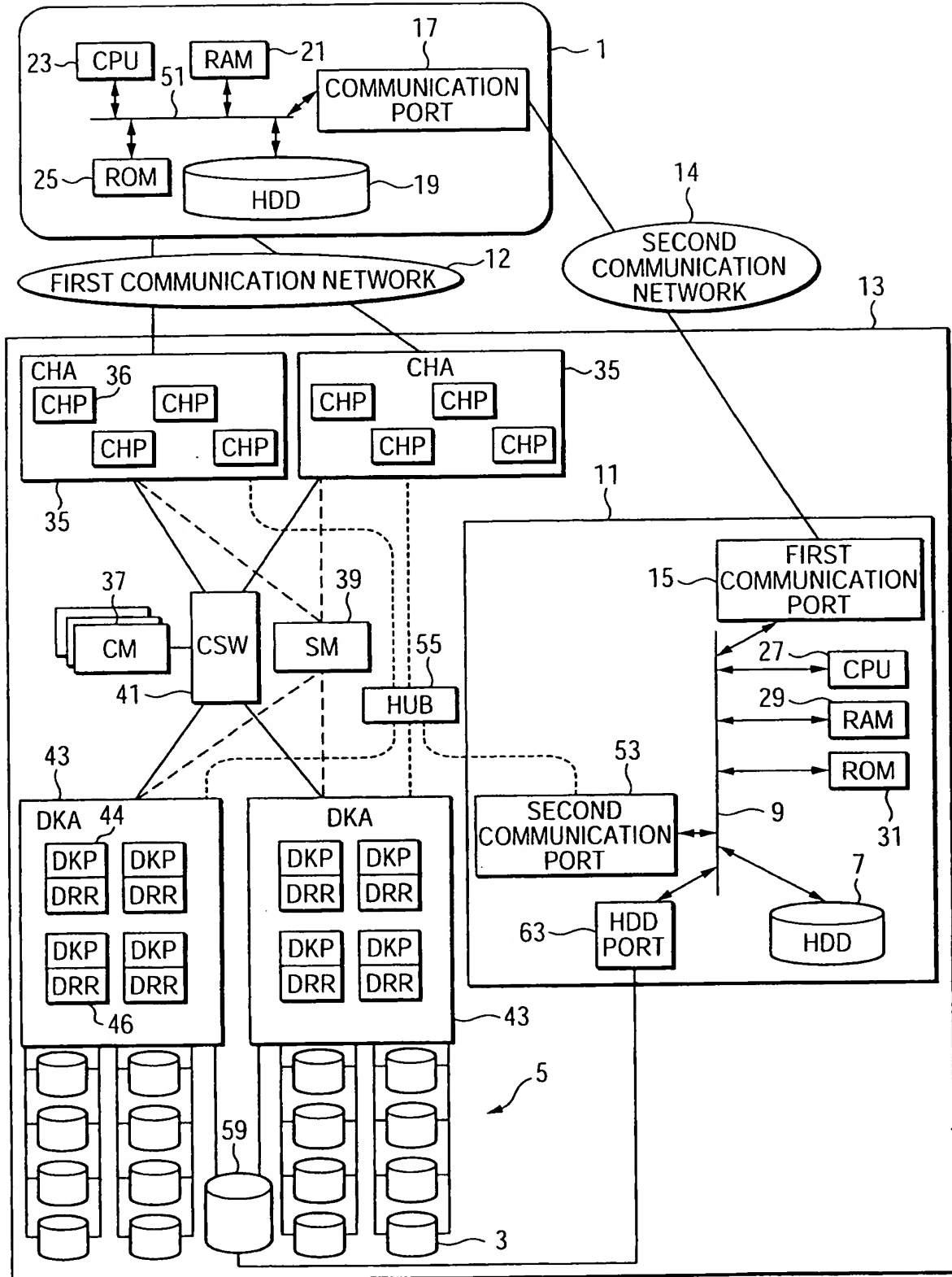
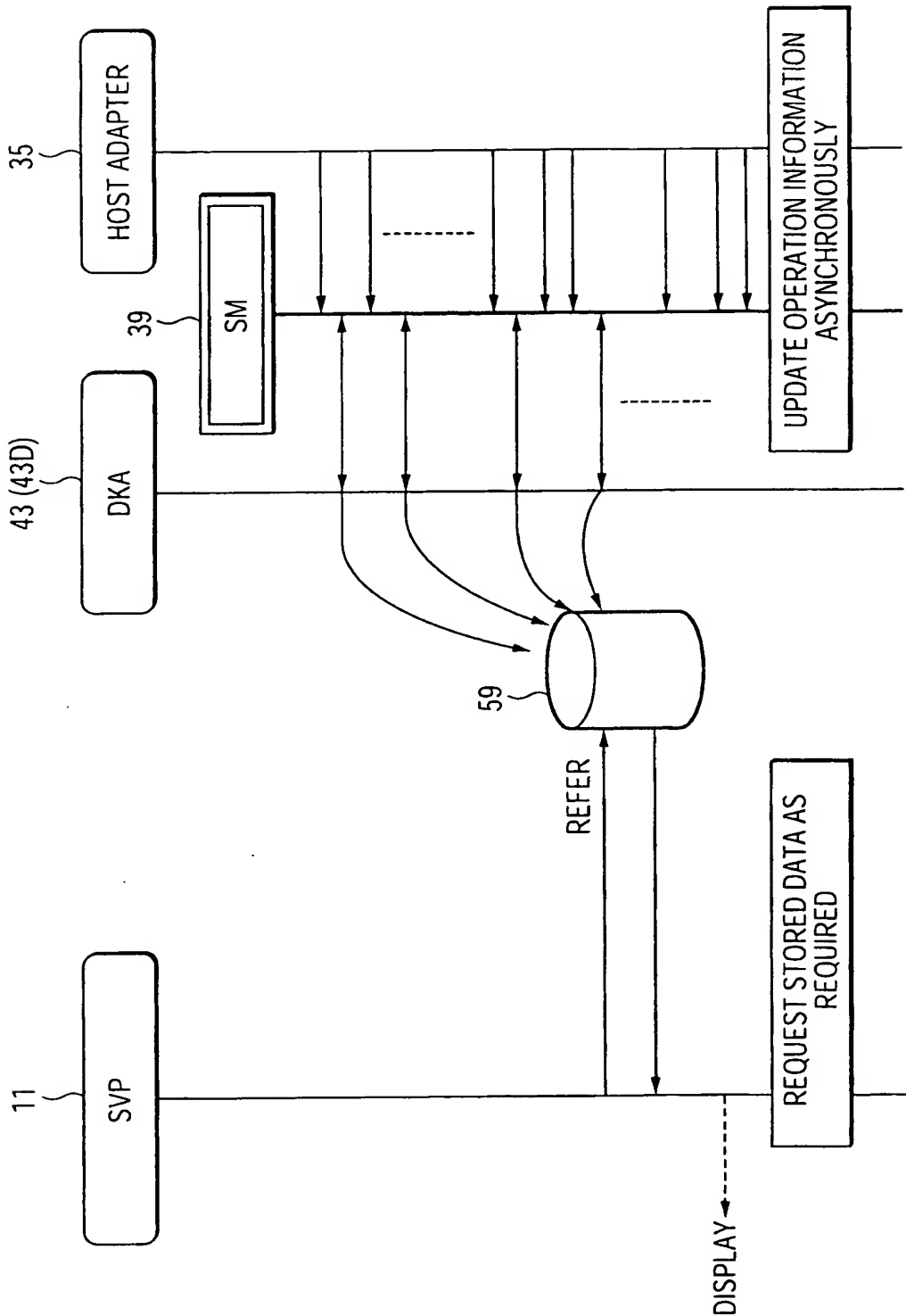


FIG. 9



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FIG. 10A

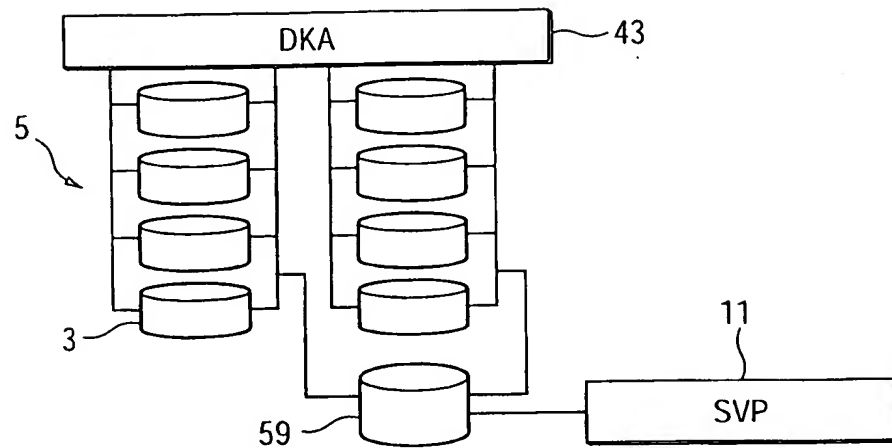


FIG. 10B

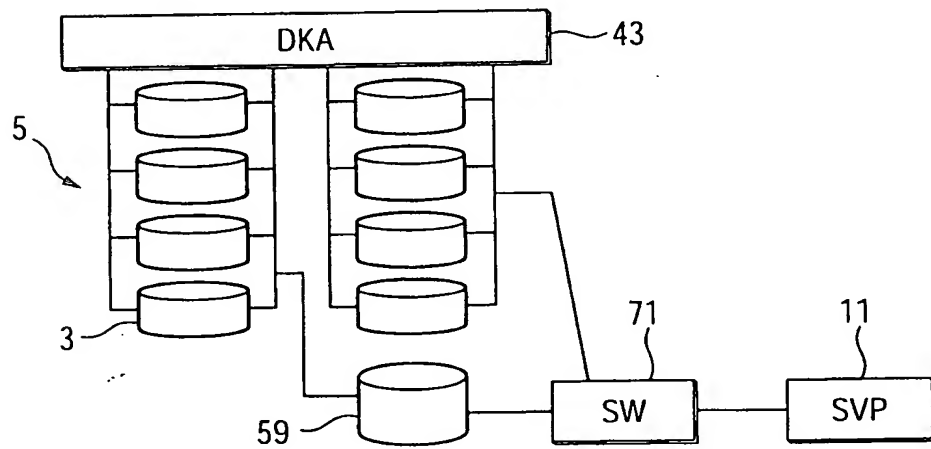


FIG. 10C

